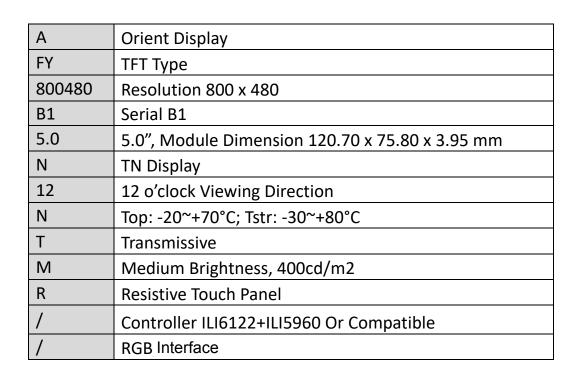


Specification for TFT

AFY800480B1-5.0N12NTM-R

Revision A





REVISION RECORD

Rev No.	Rev date	Contents	Remarks
0	2020-08-03	First release	Preliminary
А	2022-05-31	Update AC CHARACTERISTICS	Page 11

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1. GENERAL INFORMATION

No.	Item	Contents	Unit
1	LCD size	5.0 inch (Diagonal)	/
2	Display mode	TN/Normally white/Transmissive	/
3	Viewing direction(eye)	12 O'clock	/
4	Gray scale inversion direction	6 O'clock	/
5	Resolution(H*V)	800 *480 Pixels	/
6	Module size (L*W*H)	120.70*75.80*3.95	mm
7	Active area (L*W)	108.00*64.80	mm
8	Pixel pitch (L*W)	0.135*0.135	mm
9	Interface type	RGB 24bit interface	/
10	Color Depth	16.7M	/
11	Module power consumption	1.272	W
12	Back light type	LED	/
13	Driver IC	ILI6122+ILI5960 OR COMPATIBLE	/
14	Weight	72.3	G

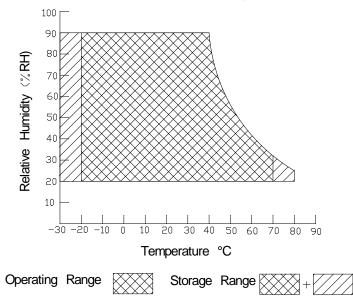
2. ABSOLUTE MAXIMUM RATINGS

Item	Symbol	Min.	Max.	Unit	Note
Power supply input voltage for TFT	VDD	-0.3	5.0	V	
Backlight current (normal temp.)	ILED	-	50	mA	
Operation temperature	Тор	-20	+70	°C	Note1
Storage temperature	Tst	-30	+80	°C	Note1
Humidity	RH	20%	90%	RH	Note1

Note1:

1). The relative humidity and temperature range are as below sketch, 90% RH Max.

2). The maximum wet bulb temperature $\leq 40^{\circ}$ C and without dewing.



3. ELECTRICAL CHARACTERISTICS

DC CHARACTERISTICS(at Ta=25°C)

Item	Symbol	Min.	Тур.	Max.	Unit	Note
Power supply input voltage	VDD	3.0	3.3	3.6	V	
I/O logic voltage	VDDIO	-	-	-	V	
Input voltage 'H' level	VIH	0.7VDDIO	-	VDDIO	V	
Input voltage 'L' level	VIL	VSS	-	0.3VDDIO	V	
Power supply current	IVDD	-	160	-	mA	
TFT gate on voltage	VGH	-	-	-	V	
TFT gate off voltage	VGL	-	-	-	V	
Analog power supply voltage	AVDD	-	-	-	V	
Differential input common mode voltage	Vcom	-	-	-	V	Note1

Note1 : The value is just the reference value. The customer can optimize the setting value by the different D-IC Vcom must be adjusted to optimize display quality, as Crosstalk and Contrast Ratio etc..

4. BACKLIGHT CHARACTERISTICS

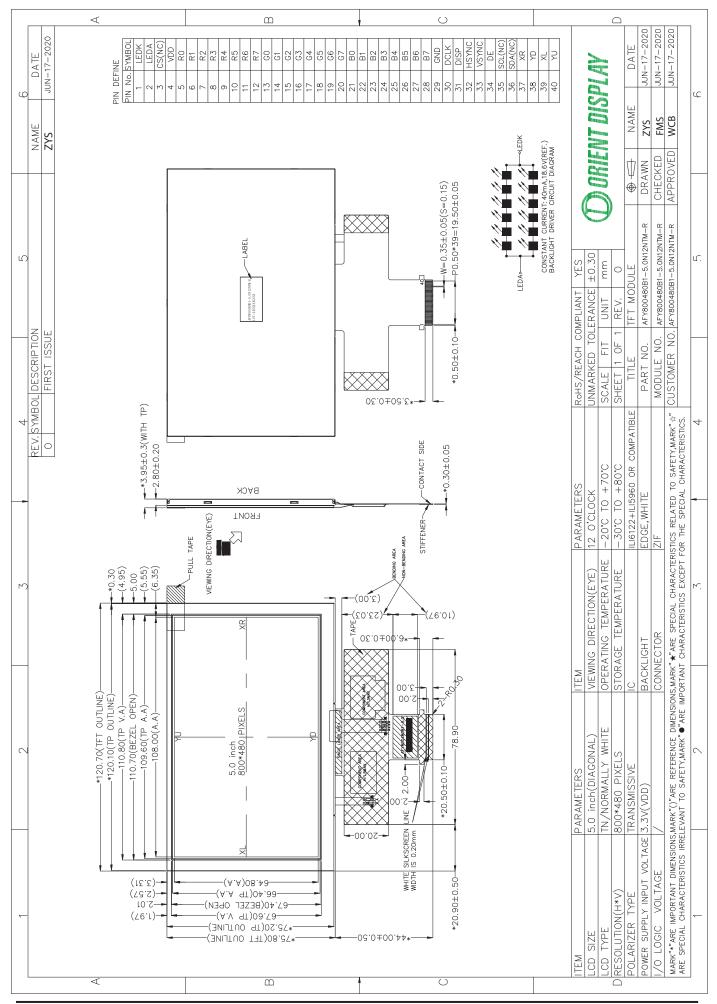
(at Ta=25°C,RH=60%)

ltem	Symbol	Min.	Тур.	Max.	Unit	Note
LED forward voltage	VF	17.2	18.6	19.8	V	
LED forward current	IF	-	40	-	mA	IF=20*2mA
LED power consumption	PLED	-	0.744	-	W	Note1
Number of LED	-		12		PCS	
Connection mode	-	6 in series 2 in parallel			/	
LED life-time	-	20000	-	-	Hrs	Note2

Note1 : Calculator value for reference : IF*VF = PLED

Note2 : The LED life-time define as the estimated time to 50% degradation of initial brightness at Ta= 25° C and IF =40mA. The LED lifetime could be decreased if operating IF is larger than 40mA.

5. EXTERNAL DIMENSIONS



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Item	Symbol	Condition	Min.	Тур.	Max.	Unit	Remark	Note	
Response time	Tr+ Tf		-	25	50	ms	FIG.1	Note 1	
Contrast ratio	Cr	-	400	500	-	-	FIG.2	Note 2	
Surface Iuminance	Lv	θ=0°	350	400	-	cd/m ²	FIG.2	Note 3	
Luminance uniformity	Yu	θ=0°	75	80	-	%	FIG.2	Note 4	
NTSC	-	θ=0°	-	50	-	%	FIG.2	Note 5	
		Ø =90°	60	70	-	deg	FIG.3	Note 6	
	θ	Ø = 270°	40	50	-	deg	FIG.3		
Viewing angle		9	Ø = 0°	60	70	-	deg	FIG.3	Note 6
		Ø=180°	60	70	-	deg	FIG.3		
	Red x			0.56		-			
	Red y			0.32		-			
	Green x	0.00		0.34		-			
CIE (x,y)	Green y	θ=0° ∅=0°	Тур	0.60	Тур	-	FIG.2	Noto F	
chromaticity	Blue x	©=0 Ta=25°C	-0.04	0.15	+0.04	-	CIE1931	Note 5	
	Blue y	Blue y		0.10		-			
	White x			0.32		-			
	White y			0.35		-			

6. ELECTRO - OPTICAL CHARACTERISTICS

Note1. Definition of response time

The response time is defined as the LCD optical switching time interval between "White" state and "Black"state.Rise time (T_{ON}) is the time between photo detector output intensity changed from 90% to 10%. And fall time (T_{OFF}) is the time between photo detector output intensity changed from 10% to 90%. For additional information see FIG1.

Note2.Definition of contrast ratio

Contrast ratio(Cr) is defined mathematically by the following formula.

For more information see FIG.2.

Contrast ratio= Luminance measured when LCD on the "White" state

Luminance measured when LCD on the "Black" state

Measured at the center area of the LCD

Note3.Definition of surface luminance

Surface luminance is the luminance with all pixels displaying white.

For more information see FIG.2.

Lv = Average Surface Luminance with all white pixels(P1,P2,P3,,Pn)

Note4.Definition of luminance uniformity

The luminance uniformity in surface luminance is determined by measuring luminance at each test position 1 through n, and then dividing the maximum luminance of n points luminance by minimum luminance of n points luminance. For more information see FIG.2.

 $Y_{u} = \frac{\text{Minimum surface luminance with all white pixels (P1, P2, P3,, Pn)}{\text{Minimum surface luminance with all white pixels (P1, P2, P3,, Pn)}$

Yu = Maximum surface luminance with all white pixels (P1,P2,P3,.....,Pn)

Note5. Definition of color chromaticity (CIE1931)

CIE (x,y) chromaticity, The x,y value is determined by screen active area center position P5. For more information see FIG.2.

Note6. Definition of viewing angle

Viewing angle is the angle at which the contrast ratio is greater than 10. angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD surface. For more information see FIG.3.

For viewing angle and response time testing, the testing data is base on Autronic-Melchers's ConoScope or DMS series Instruments or compatible. For contrast ratio, Surface Luminance, Luminance uniformity and CIE, the testing data is base on TOPCON's BM-5 or BM-7 photo detector or compatible.

FIG.1. The definition of response Time

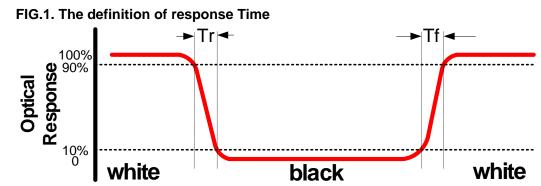


FIG.2. Measuring method for contrast ratio, surface luminance, **Iuminance uniformity, CIE (x,y) chromaticity**

H,V: Active area

Light spot size Ø= 5 mm(BM-5) or Ø=7.7mm (BM-7)50cm distance or compatible distance from the LCM surface to detector lens.

Test spot position : see Figure a.

measurement instrument : TOPCON's luminance meter BM-5 or BM-7 or compatible ,see Figure b.

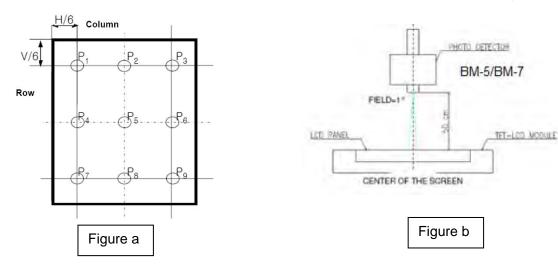
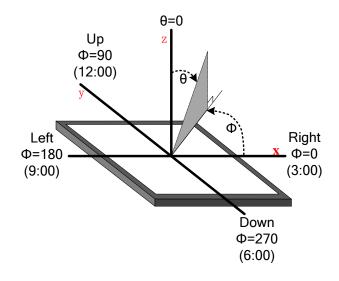


FIG.3. The definition of viewing angle



7. INTERFACE DESCRIPTION

Interface No.	Name	I/O or connect to	Description
1	LEDK	Р	Power for LED backlight(Cathode)
2	LEDA	Р	Power for LED backlight(Anode)
3	CS(NC)	/	/
4	VDD	Р	Power for LCD
5-12	Red(0-7)	I	Red data
13-20	Green(0-7)	I	Green data
21-28	Blue(0-7)	I	Blue data
29	GND	Р	Ground
30	DCLK	I	Dot clock
31	DISP	I	Display on/off
32	HSYNC	I	Horizontal sync input.
33	VSYNC	I	Vertical sync input
34	DE	I	Data enable
35	SCL(NC)	/	/
36	SDA(NC)	/	/
37	XR	I	Touch panel X right
38	YD	I	Touch panel Y down
39	XL	I	Touch panel X left
40	YU	I	Touch panel Y up

8.AC CHARACTERISTICS

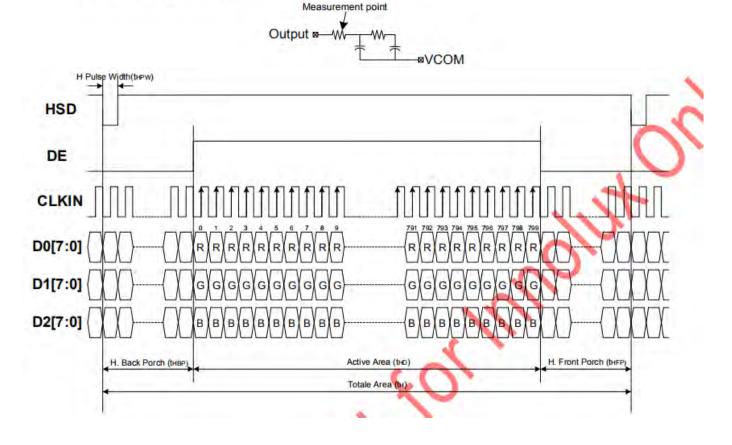
Description	Orman		Spec		11.00	Conditions	
Parameter	Symbol	Min.	Тур.	Max.	Unit		
VDD Power ON slew rate	t POR	-		20	ms	0V ~ 0.9VDD	
RSTB pulse width	tRST	10	5	-	us	CLKIN=50MHz	
CLKIN cycle time	t _{CPH}	20	÷	-	ns		
CLKIN pulse duty	t _{CWH}	40	50	60	%		
VSD setup time	t _{vst}	8	7		ns	1	
VSD hold time	t _{VHD}	8	5	N.	ns		
HSD setup time	tHST	8	Y		ns		
HSD hold time	tннр	8		ł	ns		
Data setup time	tost	8	1	4	ns	D0[7:0], D1[7:0], D2[7:0] to CLKIN	
Data hold time	t _{DHD}	8	J	Ŧ	ns	D0[7:0], D1[7:0], D2[7:0] to CLKIN	
DE setup time	LEST	8	140	1	ns		
DE hold time	t _{EHD}	8	ţ	4	ns		
Output stable time	lssr	÷.	1	6	us	10% to 90% target voltage. CL=120pF, R=10K Ω	
CLKIN frequency	f _{CLK}	-	40	50	MHz	VDD=3.0 ~ 3.6V	
CLKIN cycle time	t _{CLK}	20	25	n	ns	11	
CLKIN pulse duty	tcwn	40	50	60	%	T _{CLK}	
Time from HSD to Source output	tuso	-	20	-	CLKIN		
Time from HSD to LD	t _{HLD}	1.44	20	4	CLKIN	Note (2)	
Time from HSD to STV	t _{HSTV}		2	-	CLKIN		
Time from HSD to CKV	t _{HCKV}	-	20	1	CLKIN		
Time from HSD to OEV	t _{HOEV}	t.	4	4	CLKIN		
LD pulse width	twid	-	10	ł	CLKIN	Note (2)	
CKV pulse width	twckv	-	66	Ŧ	CLKIN		
OEV pulse width	twoev	-	74	-	CLKIN	1	

Note: (1) VDD=3.0 ~ 3.6V, VDDA=6.5~13.5V, DGND=AGND=0V, Ta=-20~+85 C

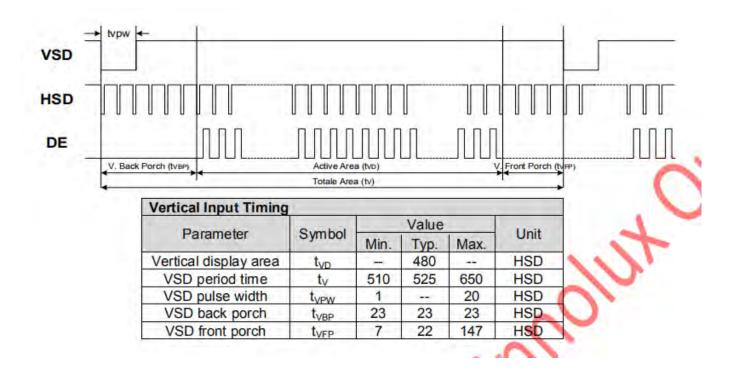
(2) The contents of the data register are transferred to the latch circuit at the rising edge of LD. Then the

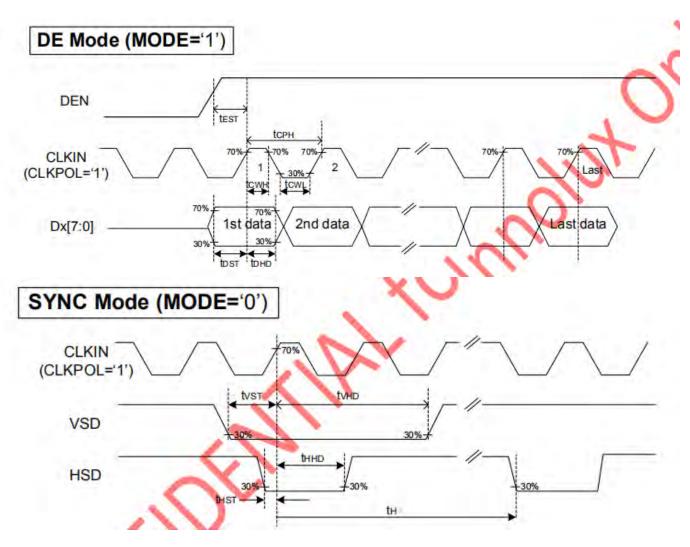
gray scale voltage is output from the device at the falling edge of LD.

(3) Output loading condition :



Parameter		Symbol		Value		Unit
		Symbol	Min.	Тур.	Max.	Unit
Horizontal disp	a t _{HD}		800		CLKIN	
CLKIN frequency		f CLK		33.3	50	MHz
1 Horizontal lin	ne period	t _H	862	1056	1200	CLKIN
	Min.	t _{HPW}		1		CLKIN
HSD pulse width	Тур.			0.000 il	1944 M	CLKIN
width	Max.		-	40	*	CLKIN
HSD back porch	SYNC	t _{нвР}	46	46	46	CLKIN
HSD front porch	SYNC	t _{HFP}	16	210	354	CLKIN





9. POWER SEQUENCE

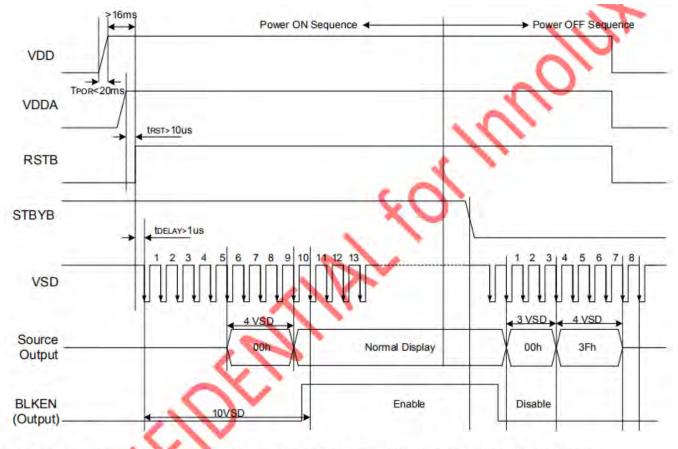
To prevent the device damage from latch up and Improve subjective display effect, the power ON/OFF sequence shown below must be followed.

To prevent the device damage from latch up, the power ON/OFF sequence shown below must be followed.

Power ON: VDD, DGND → VDDA, AGND → V1 to V14

Power OFF: V1 to V14 → VDDA, AGND→ VDD, DGND

In order to prevent ILI6122 from power ON reset fail, the rising time (t_{POR}) of the digital power supply VDD should be maintained within given specifications. The power ON/OFF timing sequence is illustrated as below:



Note: For prevent anormal operation, t_{RST} must be longer than 10us during Power ON sequence.

10. RELIABILITY TEST CONDITIONS

No.	Test item	Test con	dition	Inspection after test
11.1	High temperature storage test	+80°C/240 hours		
11.2	Low temperature storage test	-30°C/240 hours		
11.3	High temperature operating test	+70°C/120 hours	+70°C/120 hours	
11.4	Low temperature operating test	-20°C/120 hours		Inspection after
11.5	Temperature cycle storage test	-30°C ~ 25°C ~ +80°C/10cycles (30min) (10min) (30min)		2~4hours storage at room temperature, the sample shall be free
11.6	High temperature high humidity test	+50°C*90% RH/120 hours		from defects : 1.Current changing
11.7	Vibration test	Frequency : 250 r/min Amplitude : 1 inch Time: 45min		value before test and after test is 50% larger; 2. Function defect :
		Drop direction: 1 corner/3 edges/6 sides 10 times		Non-display,abnormal-d isplay,missing lines, Short lines,ITO
		Packing weight(kg)	Drop height(cm)	corrosion;
11.8	Drop test	<11	80±1.6	3.Visual defect : Air bubble in the LCD,Seal
		11≦G<21	60±1.2	leak,Glass crack.
		21≦G<31	50±1.0	
		31≦G<40	40±0.8	
11.9	ESD test		Air discharge: ±8KV, 10times Contact discharge: ±4KV, 10times	

Remark :

1. The test samples should be applied to only one test item.

2.Sample size for each test item is 3~5pcs.

3.For High temperature high humidity test, Pure water(Resistance>10MΩ) should be used.

4.In case of malfunction defect caused by ESD damage, if it would be recovered to normal state after resetting, it would be judged as a good part.

5.B/L evaluation should be excepted from reliability test with humidity and temperature: Some defects such as black spot/blemish can happen by natural chemical reaction with humidity and Fluorescence B/L has. 6.Failure judgment criterion: Basic specification, Electrical characteristic, Mechanical characteristic, Optical characteristic.

7. After the reliability test, the product only guarantees operation, but don't guarantee all of the cosmetic specification.

11.INSPECTION CRITERION

Refer to the quality standard on the back of the specification

12. HANDLING PRECAUTIONS

12.1 Mounting method

The LCD module consists of two thin glass plates with polarizes which easily be damaged. And since the module in so constructed as to be fixed by utilizing fitting holes in the printed circuit board. Extreme care should be needed when handling the LCD modules.

12.2 Caution of LCD handling and cleaning

When cleaning the display surface, Use soft cloth with solvent

[recommended below] and wipe lightly :

- •.lsopropyl alcohol
- Ethyl alcohol

Do not wipe the display surface with dry or hard materials that will damage the polarizer surface. Do not use the following solvent :

- •.Water
- Aromatics

Do not wipe ITO pad area with the dry or hard materials that will damage the ITO patterns

Do not use the following solvent on the pad or prevent it from being contaminated :

- Soldering flux
- •.Chlorine (CI) , Sulfur (S)

If goods were sent without being silicon coated on the pad, ITO patterns could be damaged due to the corrosion as time goes on.

If ITO corrosion happen by miss-handling or using some materials such as Chlorine (CI), Sulfur (S) from customer, Responsibility is on customer.

12.3 Caution against static charge

The LCD module use C-MOS LSI drivers, so we recommended that you :

Connect any unused input terminal to Vdd or Vss, do not input any signals before power is turned on, and ground your body, work/assembly areas, assembly equipment to protect against static electricity.

12.4 Packing

Module employ LCD elements and must be treated as such.

• Avoid intense shock and falls from a height.

•. To prevent modules from degradation, do not operate or store them exposed direct to sunshine or high temperature/humidity.

12.5 Caution for operation

•. It is an indispensable condition to drive LCD's within the specified voltage limit since the higher voltage then the limit cause the shorter LCD life.

•. An electrochemical reaction due to direct current causes LCD's undesirable deterioration, so that the use of direct current drive should be avoided.

•.Response time will be extremely delayed at lower temperature then the operating temperature range and on the other hand at higher temperature LCD's how dark color in them. However those phenomena do not mean malfunction or out of order with LCD's, which will come back in the specified operation temperature.

•. If the display area is pushed hard during operation, some font will be abnormally displayed but it resumes normal condition after turning off once.

•.A slight dew depositing on terminals is a cause for electro-chemical reaction resulting in terminal open circuit.

•. Usage under the maximum operating temperature, 50%Rh or less is required.

•. When fixed patterns are displayed for a long time, remnant image is likely to occur.

12.6 Storage

In the case of storing for a long period of time for instance, for years for the purpose or replacement use, the following ways are recommended.

•.Storing in an ambient temperature 10°C to 30°C, and in a relative humidity of 45% to 75%. Don't expose to sunlight or fluorescent light.

•.Storing in a polyethylene bag with the opening sealed so as not to enter fresh air outside in it . And with no desiccant.

•.Placing in a dark place where neither exposure to direct sunlight nor light's keeping the storage temperature range.

•.Storing with no touch on polarizer surface by the anything else.

It is recommended to store them as they have been contained in the inner container at the time of delivery from us.

12.7 Safety

•. It is recommendable to crash damaged or unnecessary LCD's into pieces and wash off liquid crystal by either of solvents such as acetone and ethanol, which should be burned up later.

•. When any liquid leaked out of a damaged glass cell comes in contact with your hands, please wash it off well with soap and water.

13. PRECAUTION FOR USE

13.1 A limit sample should be provided by the both parties on an occasion when the both parties agreed its necessity. Judgment by a limit sample shall take effect after the limit sample has been established and confirmed by the both parties.

13.2 On the following occasions, the handing of problem should be decided through discussion and agreement between responsible of the both parties.

•.When a question is arisen in this specification.

•.When a new problem is arisen which is not specified in this specifications.

•.When an inspection specifications change or operating condition change in customer is reported to ODNA, and some problem is arisen in this specification due to the change.

•.When a new problem is arisen at the customer's operating set for sample evaluation in the customer site.

14. PACKING SPECIFICATION

Please consult our technical department for detail information.

15. HSF COMPLIANCE

•.This products complies with ROHS 2011/65/EU and 2015/863/EU、REACH 1907/2006/EC requirements, and the packaging complies with 94-62-EC.