



**SPECIFICATION
FOR
LCD MODULE**

**MODULE NO: AFK1280800A0-10.1INTN
REVISION NO: V06**

Customer's Approval:

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	SIGNATURE	DATE
PREPARED BY (RD ENGINEER)		
CHECKED BY		
APPROVED BY		

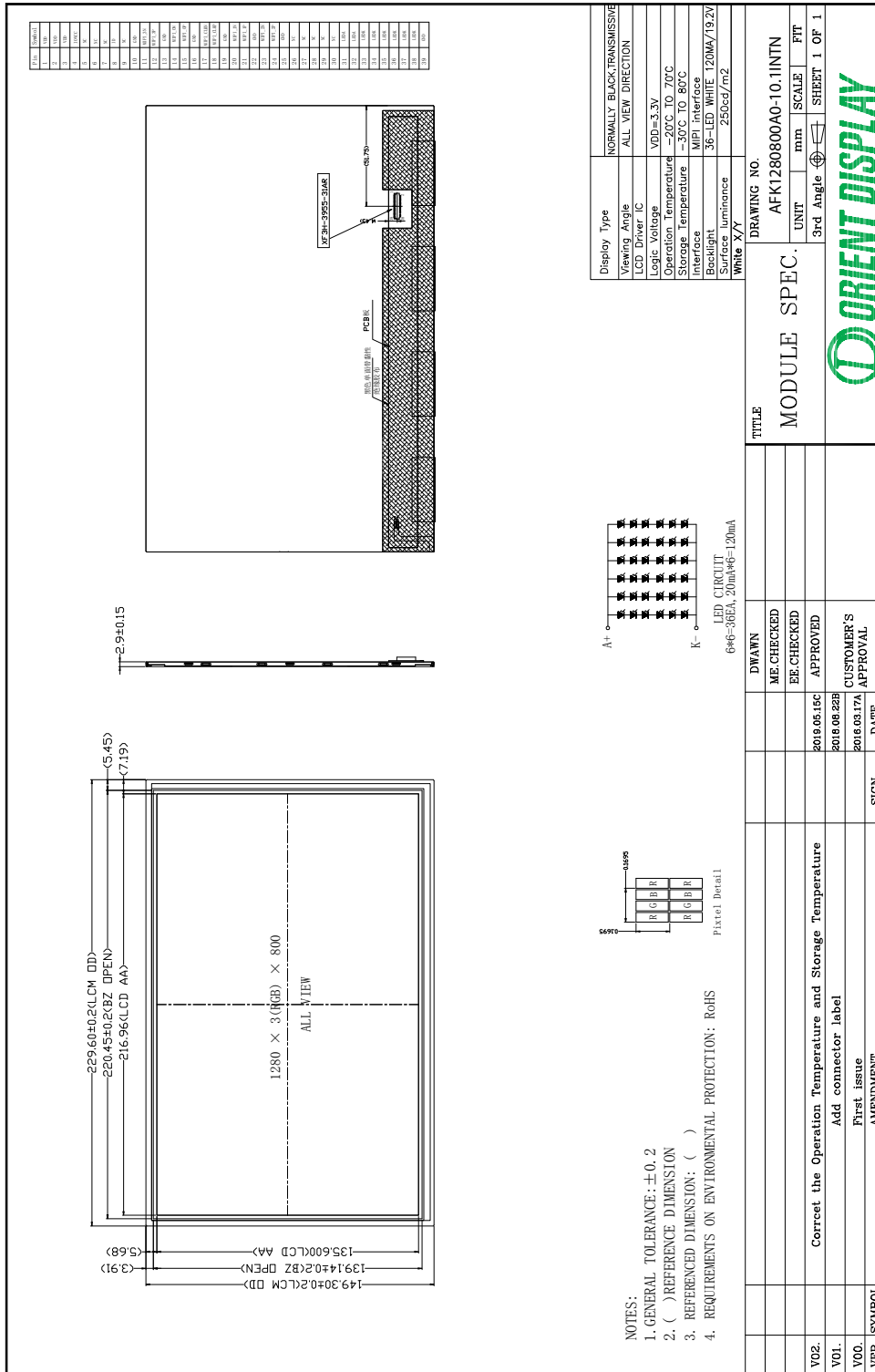
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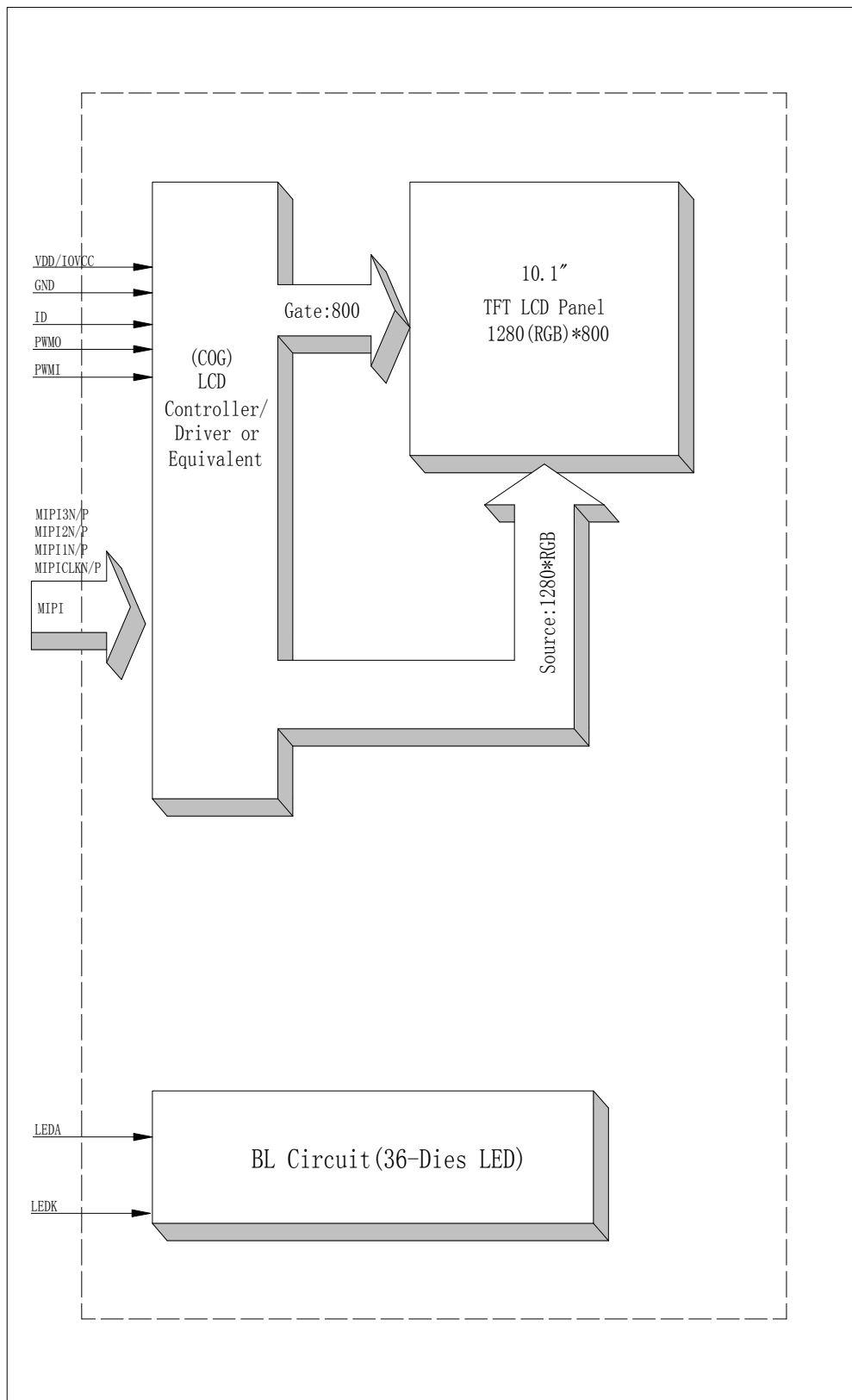
1. General Specification

Item	Contents	Unit
LCD TYPE	TFT/TRANSMISSIVE	
MODULE SIZE (W*H*T)	229.6*149.3*2.9	MM
ACTIVE SIZE (W*H)	216.96*135.6	MM
PIXEL PITCH (W*H)	0.1695*0.1695	MM
NUMBER OF DOTS	1280*800	
DRIVER IC	TBD	
INTERFACE TYPE	MIPI	
TOP POLARIZER TYPE	ANTI-GLARE	
RECOMMEND VIEWING DIRECTION	ALL	O'CLOCK
GRAY SCALE INVERSION DIRECTION	--	O'CLOCK
COLORS	16.7M	
BACKLIGHT TYPE	36-CHIP WHITE LED	
TOUCH PANEL TYPE	WITHOUT	

2. Mechanical Drawing



3. Block Diagram



4. Interface Pin Function

Pin No.	Symbol	Description
1	VDD	Logic power 3.3V
2	VDD	Logic power 3.3V
3	VDD	Logic power 3.3V
4	IOVCC	IOVCC(1.8v)
5	NC	No connection
6	NC	No connection
7	NC	No connection
8	ID	Product ID signal output (1.8V)
9	NC	NO CONNECT
10	GND	GROUND
11	MIPI_3N	MIPI DATA NEGATIVE SIGNAL(3N)
12	MIPI_3P	MIPI DATA POSITIVE SIGNAL(3P)
13	GND	GROUND
14	MIPI_0N	MIPI DATA NEGATIVE SIGNAL(0N)
15	MIPI_0P	MIPI DATA POSITIVE SIGNAL(0P)
16	GND	GROUND
17	MIPI_CLKN	MIPI CLK NEGATIVE SIGNAL(CLKN)
18	MIPI_CLKP	MIPI CLK POSITIVE SIGNAL(CLKP)
19	GND	GROUND
20	MIPI_1N	MIPI DATA NEGATIVE SIGNAL(1N)
21	MIPI_1P	MIPI DATA POSITIVE SIGNAL(1P)
22	GND	GROUND
23	MIPI_2N	MIPI DATA NEGATIVE SIGNAL(2N)
24	MIPI_2P	MIPI DATA POSITIVE SIGNAL(2P)
25	GND	GROUND
26	PWMO	TIMING CONTROLLER PWM OUTPUT SIGNAL TO LED DIRVER
27	NC	NO CONNECT
28	NC	NO CONNECT
29	NC	NO CONNECT
30	PWMI	PWM SIGNAL TO TIMING CONTROLLER
31	LEDA	LED ANODE
32	LEDA	LED ANODE
33	LEDK	LED CATHODE
34	LEDK	LED CATHODE
35	LEDK	LED CATHODE
36	LEDK	LED CATHODE
37	LEDK	LED CATHODE
38	LEDK	LED CATHODE
39	GND	GROUND

5. Absolute Maximum Ratings

Parameter	Symbol	Min	Max	Unit
Supply voltage for analog	VDD	-0.3	5	V
Supply voltage for logic	IOVCC	-0.5	5	V
Supply current (One LED)	I _{LED}		60	mA
Operating temperature	T _{OP}	-20	70	°C
Storage temperature	T _{ST}	-30	80	°C

Note: The absolute maximum rating values of this product are not allowed to be exceeded at any times. Should a module be used with any of the absolute maximum ratings exceeded, the characteristics of the module may not be recovered, or in an extreme case, the module may be permanently destroyed.

6. Electrical Characteristics

6.1 Input Power

Item	Symbol	Min	Typ.	Max	Unit	Applicable terminal
Supply Voltage for Analog	VDD	3.0	3.3	3.6	V	
Supply Voltage for Logic	IOVCC	3.0	3.3	3.6	V	
Input Voltage	V _{IL}	GND	-	0.3 IOVCC	V	
	V _{IH}	0.7 IOVCC	-	IOVCC		
Input leakage Current	I _{LKG}	-		-	μA	

6.2 Backlight Driving Conditions

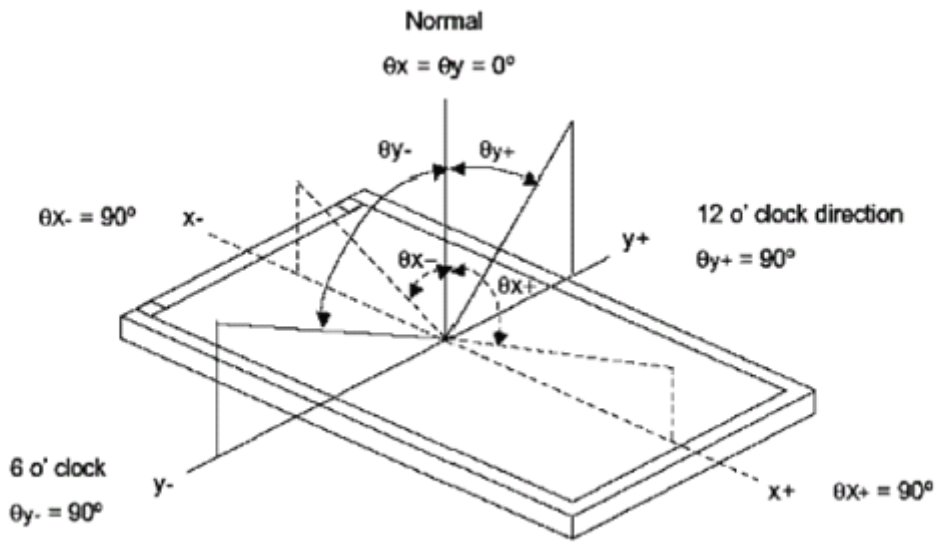
Item	Symbol	Value			Unit	Remark
		Min.	Typ.	Max.		
Voltage for LED Backlight	V _F	16.8	19.2	21	V	I _L =120mA
Current for LED Backlight	I _L		120		mA	
Power Consumption	P		2.304		W	
LED Life Time		30,000			Hr	Note

Note: Brightness to be decreased to 50% of the initial value at ambient temperature TA=25°C

7. Optical Characteristics

ITEM	SYMBOL	CONDITIONS	SPECIFICATIONS			UNIT	NOTE
			MIN	TYP.	MAX		
Luminance	L	$I_L = 120\text{mA}$	200	250	350	Cd/m^2	
Contrast Ratio	CR	$\theta = 0^\circ$	700	900			
Response Time	T_{ON}	25°C		30		ms	
	T_{OFF}						
CIE Color Coordinate	Red	X_R	Viewing normal angle				
		Y_R					
	Green	X_G					
		Y_G					
	Blue	X_B					
		Y_B					
	White	X_W		0.265	0.305	0.345	
		Y_W		0.302	0.342	0.382	
Viewing Angle	Hor.	θ_{x+}	$\text{CR} \geq 10$	80		Degree	
		θ_{x-}		80			
	Ver.	θ_{y+}		80			
		θ_{y-}		80			
Uniformity	Un		70	75		%	

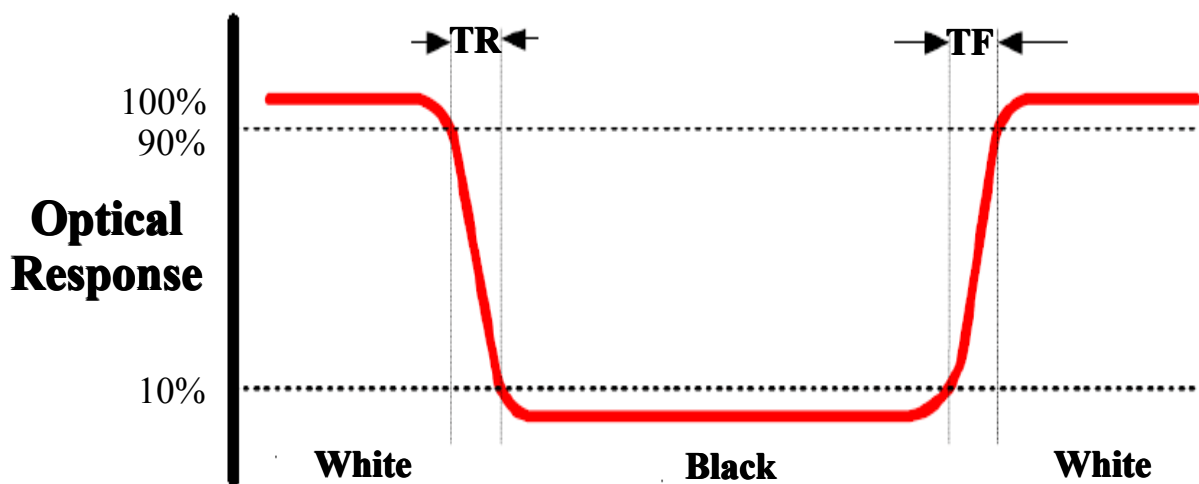
Note 1: Definition of Viewing Angle θ_x and θ_y :



Note 2: Definition of contrast ratio CR:

$$CR = \frac{\text{Luminance of white state}}{\text{Luminance of black state}}$$

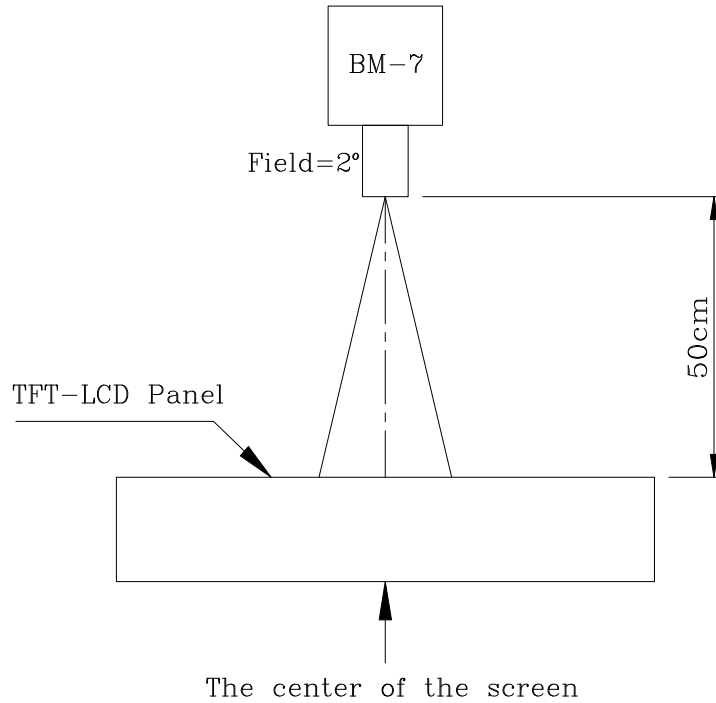
Note 3: Definition of Response Time (T_r, T_f)



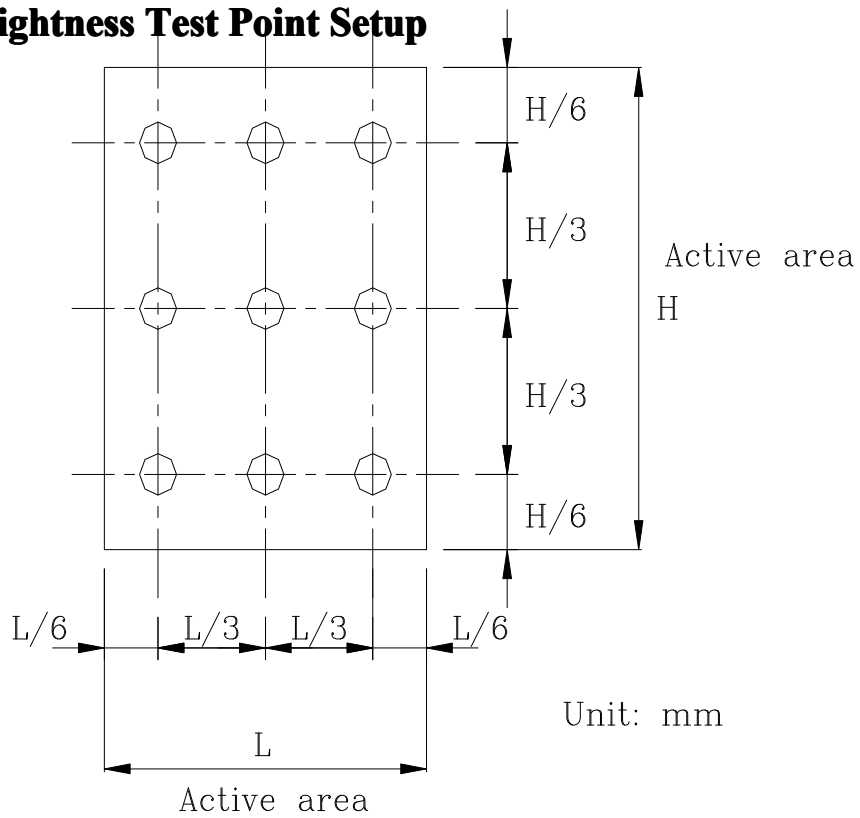
Note 4: Definition of Luminance

① The Brightness Test Equipment Setup

Field=2° (As measuring “black” image, field=2° is the best testing condition)

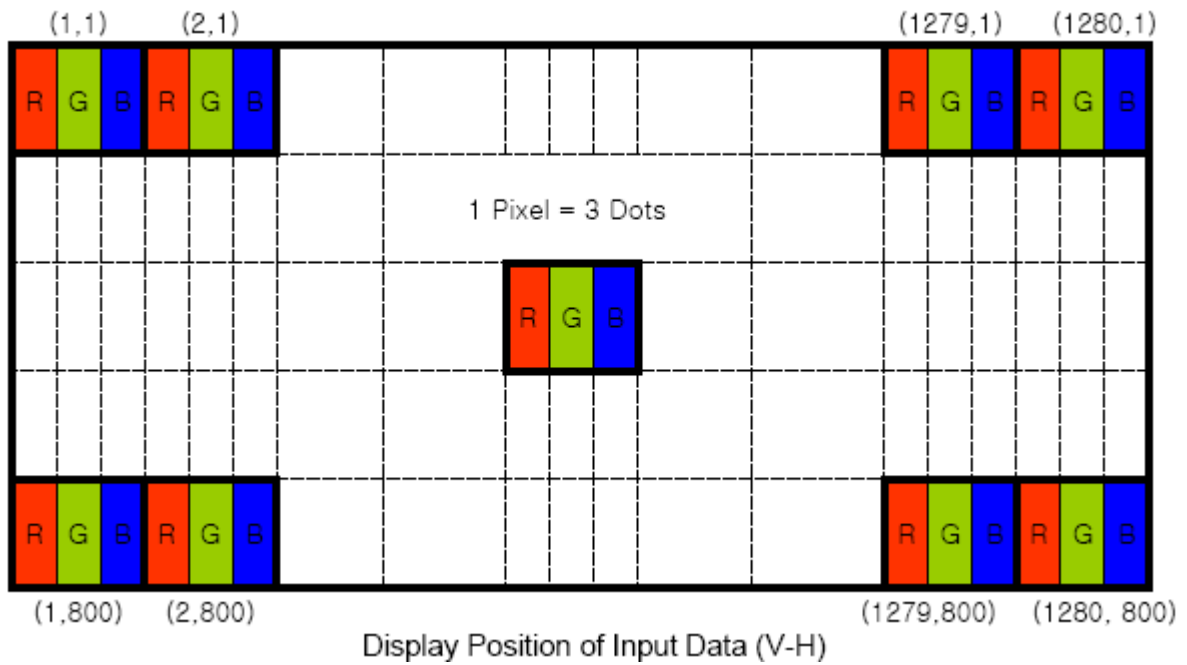


② The Brightness Test Point Setup



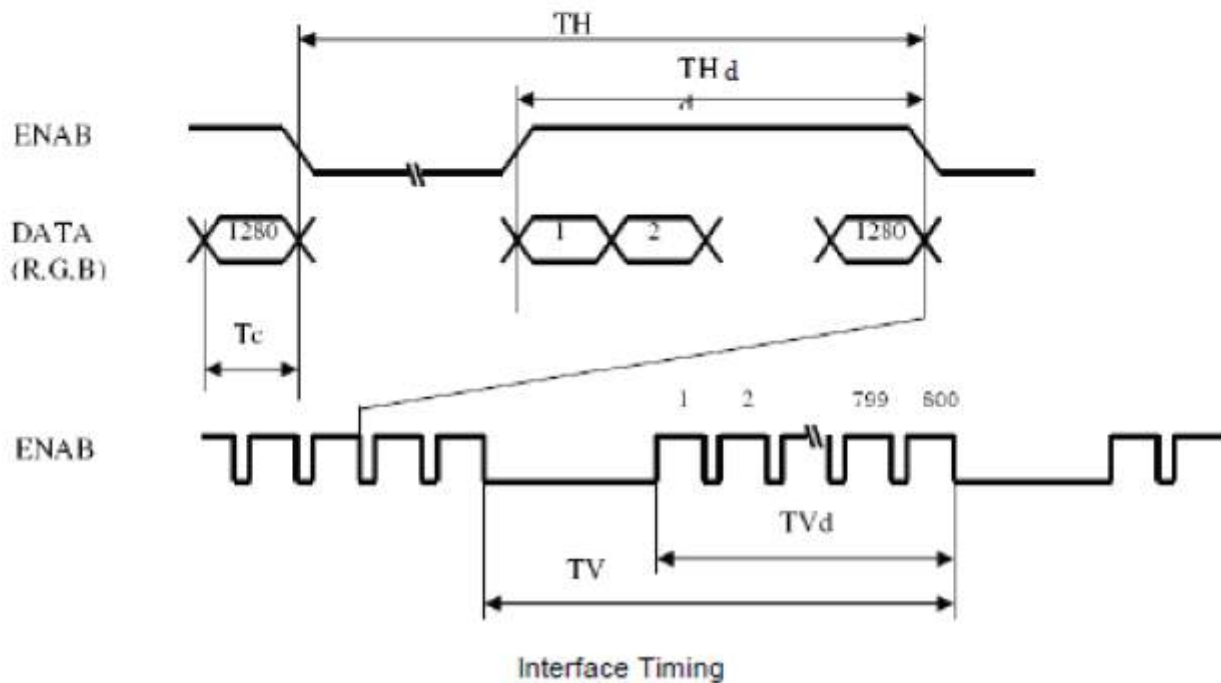
8. Timing Characteristics

8.1 Data input format



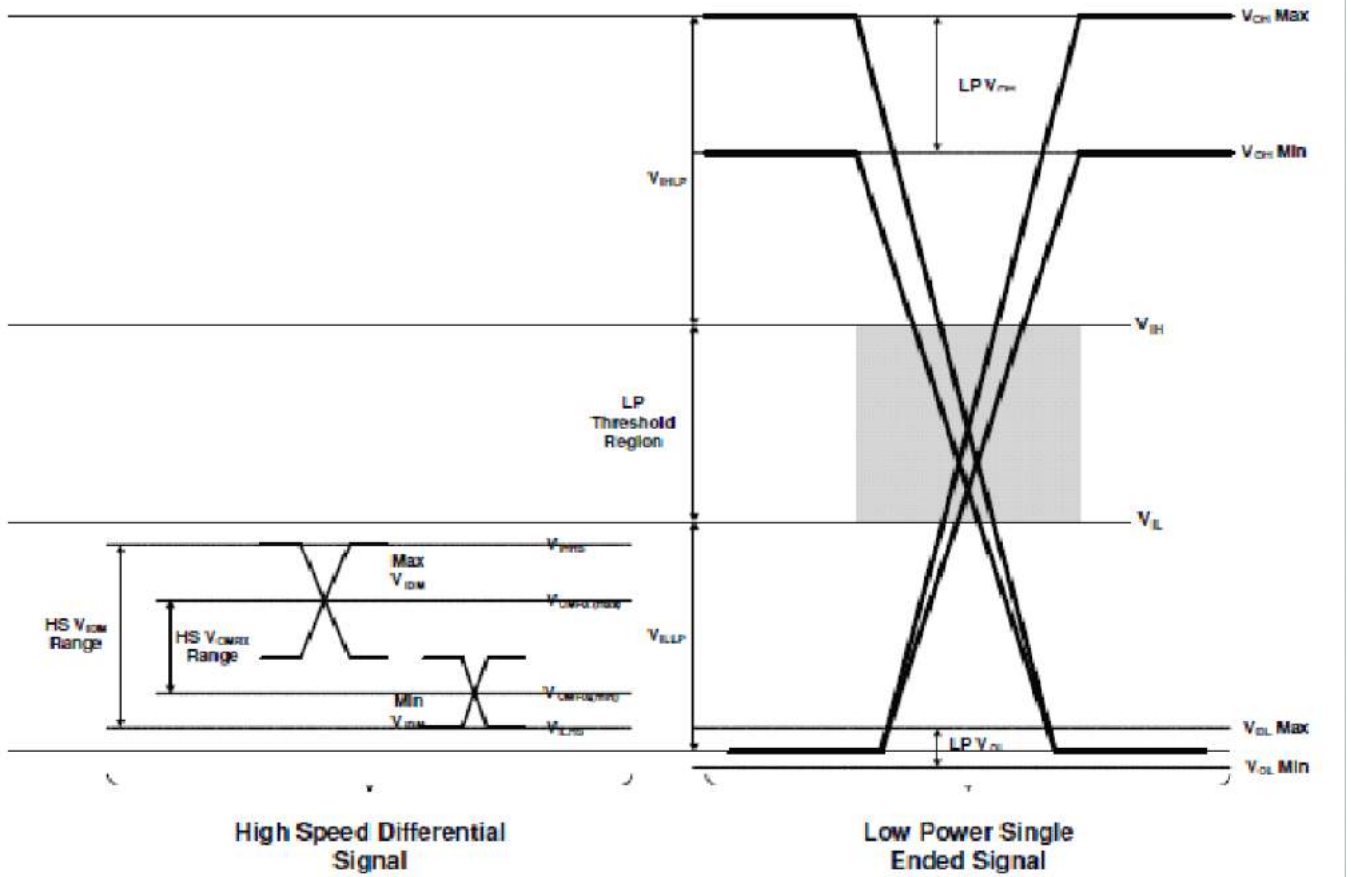
8.2 Signal timing

ITEM	Symbol		Min	Typ	Max	Unit	Note
CLK	Period	t_{CLK}	4		4.44	ns	
	Frequency	-		450	500	Mbps	
Hsync	Period	t_{HP}	-	1330	-	t_{CLK}	
	Frequency	f_H	-	48.72	-	KHz	
Vsync	Period	t_{VP}	-	812	-	t_{HP}	
	Frequency	f_V	-	60	-	Hz	
Horizontal Active Display Term	Valid	t_{HV}	-	1280	-	t_{CLK}	
	Total	t_{HP}	-	1330	-	t_{CLK}	
Vertical Active Display Term	Valid	t_{VV}	-	800	-	t_{HP}	
	Total	t_{VP}	-	812	-	t_{HP}	



8.3 MIPI Rx Interface Timing Parameter

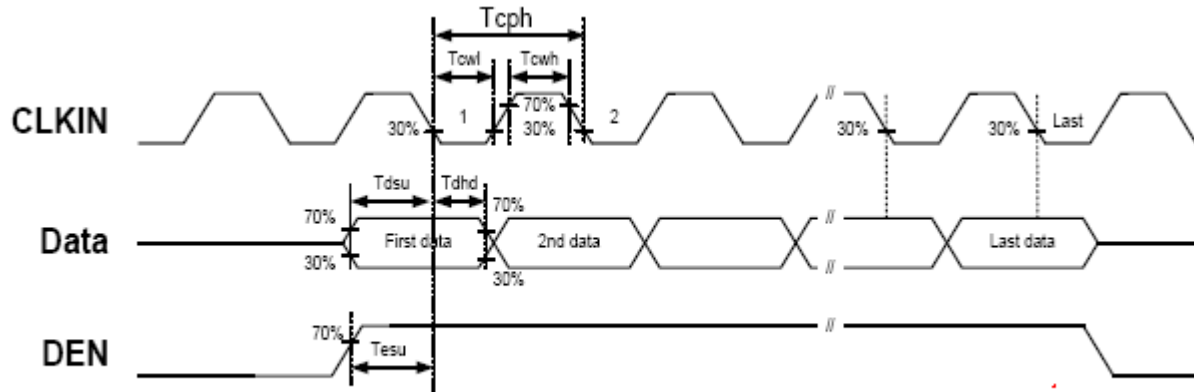
The specification of the MIPI Rx interface timing parameter is shown in Table 8.
 MIPI Rx Interface Timing Specification



MIPI Receiver Differential Input (DC Characteristics)						
Symbol	Description	Min.	Typ.	Max.	Unit	Condition
BR _{MIPI}	Input data bit rate	200	-	1000	Mbps	
V _{CMRX}	Common-mode voltage(HS Rx mode)	70	-	330	mV	
V _{IDTH}	Differential input high threshold (HS Rx mode)	-	-	70	mV	
V _{IDTL}	Differential input low threshold (HS Rx mode)	-70	-	-	mV	
V _{IDM}	Differential input voltage range (HS Rx mode)	70	-	500	mV	
V _{IHHS}	Single-end input high voltage (HS Rx mode)	-	-	460	mV	
V _{ILHS}	Single-end input low voltage (HS Rx mode)	-40	-	-	mV	
Z _{ID}	Differential input impedance	80	100	125	ohm	
V _{IHLP}	Logic 1 input voltage (LP Rx mode)	880	-	-	mV	
V _{ILLP}	Logic 0 input voltage (LP Rx mode)	-	-	550	mV	
VOH	Output high level (LP Tx mode)	1.08	1.2	1.32	V	
VOL	Output low level (LP Tx mode)	-50	-	50	mV	

MIPI Receiver Differential Input (DC Characteristics)						
Symbol	Description	Min.	Typ.	Max.	Unit	Condition
T _{MIN-RX}	Minimum pulse width response (LP Rx mode)	50	-	-	ns	
T _{L_P-PULS E-TX}	Pulse width of the LP exclusive-OR clock	50	55	58	ns	1st clock pulse after STOP state or last clock pulse before STOP state/all other pulse
T _{RLP/TFLP}	15%~85% rise time and fall time (LP Tx mode)	-	-	25	ns	
T _{REOT}	30%~85% rise time and fall time of EOT (LP Tx mode)	-	-	35	ns	
T _{L_P-PER-T X}	Period of the LP exclusive-OR clock	90	-	-	ns	
T _{SETUP}	Data to clock setup time	0.15	-	-	UI	
T _{HOLD}	Data to clock hold time	0.15	-	-	UI	

8.4 MIPI Rx Interface Timing Parameter



9. Standard Specification for Reliability

9.1 Standard Specification for Reliability of LCD Module

No	Test Item	Condition	Remarks
1	High Temperature Operation	Ts = +70°C, 240 hours	IEC60068-21:2007 GB2423.2-2008
2	Low Temperature Operation	Ta = -20°C, 240 hours	IEC60068-2-1:2007 GB/2423.1-2008
3	High Temperature Storage	Ta = +80°C, 240 hours	IEC60068-21:2007 GB/2423.2-2008
4	Low Temperature Storage	Ta = -30°C, 240 hours	IEC60068-21:2007 GB/2423.1-2008
5	Storage at High Temperature and Humidity	Ta = +60°C, 90% RH max,240hours	IEC60068-2-78 :2001 GB/T2423.3—2006
6	Thermal Shock (non-operation)	-30°C 30 min~+80°C 30 min, Change time:5min, 20 Cycle	Start with cold temperature, End with high temperature, IEC60068-214:1984, GB/2423.22-2002
7	ESD	C=150pF,R=330Ω,5point/panel Air:±8Kv,5times; Contact:±4Kv,5times (Environment: 15°C~35°C, 30%~60%.86Kpa~106Kpa)	IEC61000-42:2001 GB/T17626.2-2006
8	Vibration Test	Frequency range:10~55Hz Stroke:1.5mm Sweep:10Hz~55Hz~10Hz 2 hours for each direction of X.Y.Z (6 hours for total)	IEC60068-2-6:1982 GB/T2423.101995
9	Mechanical Shock (Non Op)	Half Sine Wave60G 6ms, ±X,±Y,±Z 3times for each direction	IEC60068-2-27:1987 GB/T2423.5—1995
10	Package Drop Test	Height:80cm, 1corner,3 edges,6 surfaces	IEC60068-2-32:1990 GB/T2423.8—1995

Note1: Ts is the temperature of panel's surface.

Note2: Ta is the ambient temperature of sample.

9.2 Testing Conditions and Inspection Criteria

For the final test, the testing sample must be stored at room temperature for 24 hours. After the tests listed in Table 9.2, standard specifications for reliability will be executed in order to ensure stability.

No.	Item	Test Model	In section Criteria
01	Current Consumption	Refer To Specification	The current consumption should conform to the product specification.
02	Contrast	Refer To Specification	After the tests have been executed, the contrast must be larger than half of its initial value prior to the tests.
03	Appearance	Visual inspection	Defect free.

9.3 MTBF

MTBF	Functions, performance, appearance, etc. shall be free from remarkable deterioration within 50,000 hours under ordinary operating and storage conditions room temperature ($25\pm 5^{\circ}\text{C}$), normal humidity ($50\pm 10\%$ RH), and in area not exposed to direct sun light.
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10. Specification of Quality Assurance

This standard of Quality Assurance confirms to the quality of LCD module products supplied by ODNA.

10.1 Quality Test

Before delivering, the supplier should conduct the following tests to confirm the quality of products.

Electrical-Optical Characteristics: According to the individual specification to test the product.

Appearance Characteristics: According to the individual specification to test the product.

Reliability Characteristics: According to the definition of reliability on the specification for testing products.

10.2 Delivery Test

Before delivering, the supplier should conduct the delivery test.

Test method: According to MIL-STD105E.General Inspection Level II take a single Time.

The defects classify of AQL as following:

Major defect: AQL = 0.65

Minor defect: AQL = 1.5

Total defects: AQL = 1.5

10.3 Non-conforming Analysis & Deal With Manners

10.3.1 Non-conforming Analysis

Purchaser should provide the data detail of non-conforming sample and the non-conforming.

After receiving the data detail from purchaser, the analysis of non-conforming should be finished within two weeks.

If the analysis can't be finished on time, supplier must notice purchaser 3 days in advance.

10.3.2 Disposition of non-conforming

If any product defect be found during assembling, supplier must change the good for every defect after confirmation.

Both supplier and customer should analyze the reason and discuss the disposition of non-conforming when the reason of nonconforming is not sure.

10.4 Agreement items

Both parties should negotiate together when the following problems happen.
There is any problem of standard of quality assurance, and both sides should agree that it must be modified.

There is any argument item which does not record in the standard of quality assurance.

Any other special problem.

10.5 Standard of The Product Appearance Test

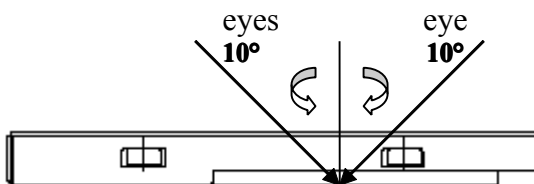
10.5.1 Manner of appearance test

The test must be under 20W × 2 or 40W fluorescent light, and the distance of view must be at 30±5cm.

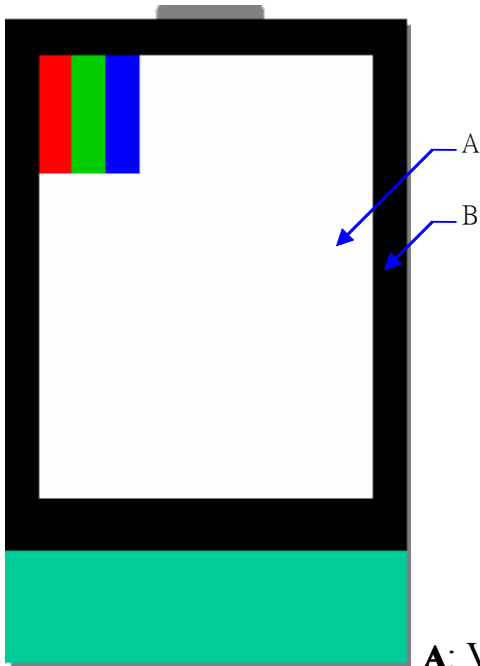
When test the model of transmissive product must add the reflective plate.

The test direction is base on around 10° of vertical line.

Temperature: 25±5°C Humidity: 60±10%RH



Definition of area:



A: Viewing area B: Outside viewing area

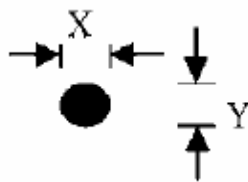
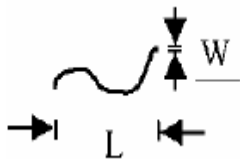
10.5.2 Basic principle

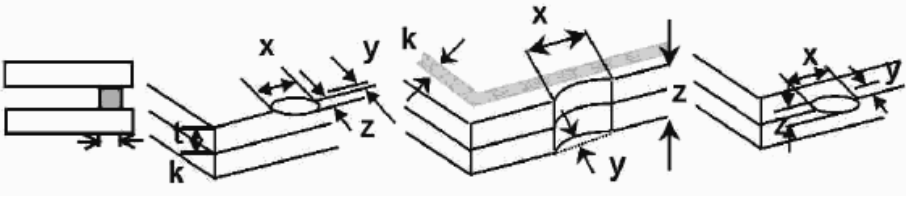
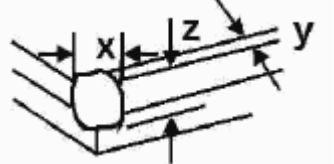
When the standard can not be described, AQL will be applied.

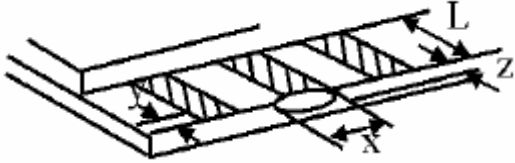
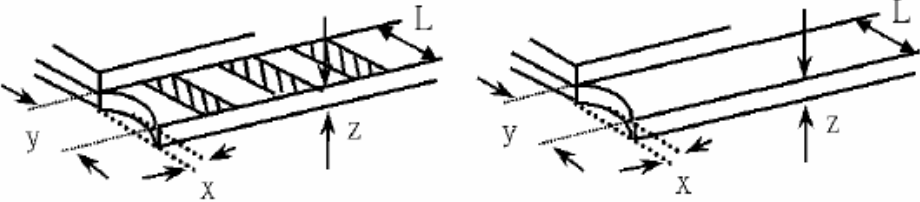
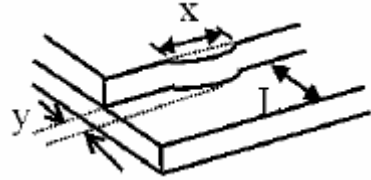
The sample of the lowest acceptable quality level must be negotiated by both supplier and customer when any dispute happened.

New item must be added on time when it is necessary.

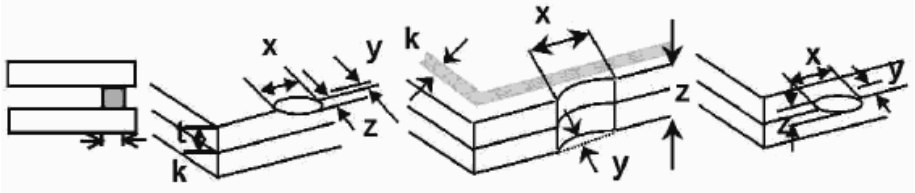
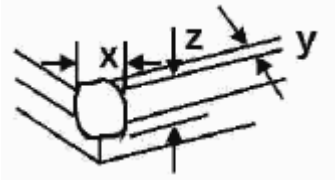
10.6 Inspection Specification

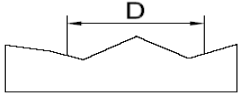
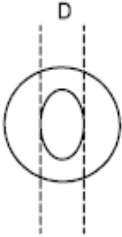
NO.	Item	Criterion	AQL												
01	Electrical Testing	1.1 Missing vertical, horizontal segment, segment contrast defect. 1.2 Missing character, dot or icon. 1.3 Display malfunction. 1.4 No function or no display. 1.5 Current consumption exceeds product specifications. 1.6 LCD viewing angle defect. 1.7 Mixed product types. 1.8 Flicker	0.65												
02	Black or White spots or Bright spots or Color spots on LCD (Display only)	2.1 White and black or color spots on display < 0.25mm, no more than one spots. 2.2 Densely spaced: No more than three spots within 3mm.	1.5												
03	LCD and Touch Panel black spots, white spots, contamination (non – display)	3.1 Round type: As following drawing $\Phi = (X+Y) / 2$  <table border="1" data-bbox="821 1086 1348 1332"> <thead> <tr> <th>Size(mm)</th> <th>Acceptable Q'ty</th> </tr> </thead> <tbody> <tr> <td>$\Phi \leq 0.10$</td> <td>Accept no dense</td> </tr> <tr> <td>$0.10 < \Phi \leq 0.20$</td> <td>1</td> </tr> <tr> <td>$0.20 < \Phi \leq 0.25$</td> <td>1</td> </tr> <tr> <td>$0.25 < \Phi \leq 0.30$</td> <td>1</td> </tr> <tr> <td>$0.30 < \Phi$</td> <td>0</td> </tr> </tbody> </table> <p>* Densely spaced: No more than two spots within 3mm.</p>	Size(mm)	Acceptable Q'ty	$\Phi \leq 0.10$	Accept no dense	$0.10 < \Phi \leq 0.20$	1	$0.20 < \Phi \leq 0.25$	1	$0.25 < \Phi \leq 0.30$	1	$0.30 < \Phi$	0	1.5
		Size(mm)	Acceptable Q'ty												
$\Phi \leq 0.10$	Accept no dense														
$0.10 < \Phi \leq 0.20$	1														
$0.20 < \Phi \leq 0.25$	1														
$0.25 < \Phi \leq 0.30$	1														
$0.30 < \Phi$	0														
3.2 Line type: (As following drawing)  <table border="1" data-bbox="726 1489 1348 1713"> <thead> <tr> <th>Length(mm)</th> <th>Width(mm)</th> <th>Acceptable Q'ty</th> </tr> </thead> <tbody> <tr> <td>---</td> <td>$W \leq 0.02$</td> <td>Accept no dense</td> </tr> <tr> <td>$L < 2.5$</td> <td>$W < 0.08$</td> <td>1</td> </tr> <tr> <td>---</td> <td>$0.08 \leq W$</td> <td>Rejection</td> </tr> </tbody> </table> <p>* Densely spaced: No more than two lines within 3mm.</p>	Length(mm)	Width(mm)	Acceptable Q'ty	---	$W \leq 0.02$	Accept no dense	$L < 2.5$	$W < 0.08$	1	---	$0.08 \leq W$	Rejection	1.5		
Length(mm)	Width(mm)	Acceptable Q'ty													
---	$W \leq 0.02$	Accept no dense													
$L < 2.5$	$W < 0.08$	1													
---	$0.08 \leq W$	Rejection													

NO.	Item	Criterion	AQL																		
04	Polarizer bubbles	If bubbles are visible, judge using black spot specifications, not easy to find, must check in specify direction <table border="1" data-bbox="906 315 1145 622"> <thead> <tr> <th>Size Φ(mm)</th> <th>Acceptable Q'ty</th> </tr> </thead> <tbody> <tr> <td>$\Phi \leq 0.30$</td> <td>Accept no dense</td> </tr> <tr> <td>$0.30 < \Phi \leq 0.50$</td> <td>0</td> </tr> <tr> <td>$0.50 < \Phi \leq 1.00$</td> <td>0</td> </tr> <tr> <td>$1.00 < \Phi$</td> <td>0</td> </tr> <tr> <td>Total Q'ty</td> <td>0</td> </tr> </tbody> </table>	Size Φ (mm)	Acceptable Q'ty	$\Phi \leq 0.30$	Accept no dense	$0.30 < \Phi \leq 0.50$	0	$0.50 < \Phi \leq 1.00$	0	$1.00 < \Phi$	0	Total Q'ty	0	1.5						
		Size Φ (mm)	Acceptable Q'ty																		
		$\Phi \leq 0.30$	Accept no dense																		
		$0.30 < \Phi \leq 0.50$	0																		
		$0.50 < \Phi \leq 1.00$	0																		
		$1.00 < \Phi$	0																		
Total Q'ty	0																				
05	Scratches	Follow NO.3 -2 Line Type.																			
06	Chipped glass	Symbols: x: Chip length y: Chip width z: Chip thickness k: Seal width t: Glass thickness a: LCD side length L: Electrode pad length 6.1 General glass chip: 6.1.1 Chip on panel surface and crack between panels:  <table border="1" data-bbox="395 1137 1214 1294"> <thead> <tr> <th>z: Chip thickness</th> <th>y: Chip width</th> <th>x: Chip length</th> </tr> </thead> <tbody> <tr> <td>$Z \leq 1/2t$</td> <td>Not over viewing area</td> <td>$x \leq 1/8a$</td> </tr> <tr> <td>$1/2t < z \leq 2t$</td> <td>Not exceed 1/3k</td> <td>$x \leq 1/8a$</td> </tr> </tbody> </table> <ul style="list-style-type: none"> ⊙ Unit: mm ⊙ If there are 2 or more chips, x is the total length of each chip 6.1.2 Corner crack:  <table border="1" data-bbox="395 1630 1214 1787"> <thead> <tr> <th>z: Chip thickness</th> <th>y: Chip width</th> <th>x: Chip length</th> </tr> </thead> <tbody> <tr> <td>$Z \leq 1/2t$</td> <td>Not over viewing area</td> <td>$x \leq 1/8a$</td> </tr> <tr> <td>$1/2t < z \leq 2t$</td> <td>Not exceed 1/3k</td> <td>$x \leq 1/8a$</td> </tr> </tbody> </table> <ul style="list-style-type: none"> ⊙ Unit: mm ⊙ If there are 2 or more chips, x is the total length of each chip 	z: Chip thickness	y: Chip width	x: Chip length	$Z \leq 1/2t$	Not over viewing area	$x \leq 1/8a$	$1/2t < z \leq 2t$	Not exceed 1/3k	$x \leq 1/8a$	z: Chip thickness	y: Chip width	x: Chip length	$Z \leq 1/2t$	Not over viewing area	$x \leq 1/8a$	$1/2t < z \leq 2t$	Not exceed 1/3k	$x \leq 1/8a$	1.5
		z: Chip thickness	y: Chip width	x: Chip length																	
		$Z \leq 1/2t$	Not over viewing area	$x \leq 1/8a$																	
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z: Chip thickness	y: Chip width	x: Chip length																			
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$1/2t < z \leq 2t$	Not exceed 1/3k	$x \leq 1/8a$																			

NO.	Item	Criterion	AQL																
07	Glass crack	<p>Symbols: x: Chip length y: Chip width z: Chip thickness k: Seal width t: Glass thickness a: LCD side length L: Electrode pad length</p> <p>7.2 Protrusion over terminal: 7.2.1 Chip on electrode pad:</p>  <table border="1" data-bbox="560 723 1236 869"> <tr> <td>y: Chip width</td> <td>x: Chip length</td> <td>z: Chip thickness</td> </tr> <tr> <td>$y \leq 0.5\text{mm}$</td> <td>$x \leq 1/8a$</td> <td>$0 < z \leq t$</td> </tr> </table> <p>7.2.2 Non-conductive portion:</p>  <table border="1" data-bbox="560 1238 1236 1384"> <tr> <td>y: Chip width</td> <td>x: Chip length</td> <td>z: Chip thickness</td> </tr> <tr> <td>$y \leq L$</td> <td>$x \leq 1/8a$</td> <td>$0 < z \leq t$</td> </tr> </table> <p>⊙ If there chipped area touches the ITO terminal, over 2/3 of the ITO must remain and be inspected according to electrode terminal specifications. ⊙ If the product will be heat sealed by the customer, the alignment mark must not be damaged.</p> <p>7.2.3 Substrate protuberance and internal crack</p>  <table border="1" data-bbox="890 1709 1326 1854"> <tr> <td>y: width</td> <td>x: length</td> </tr> <tr> <td>$y \leq 1/3L$</td> <td>$X \leq a$</td> </tr> </table>	y: Chip width	x: Chip length	z: Chip thickness	$y \leq 0.5\text{mm}$	$x \leq 1/8a$	$0 < z \leq t$	y: Chip width	x: Chip length	z: Chip thickness	$y \leq L$	$x \leq 1/8a$	$0 < z \leq t$	y: width	x: length	$y \leq 1/3L$	$X \leq a$	1.5
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$y \leq 0.5\text{mm}$	$x \leq 1/8a$	$0 < z \leq t$																	
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y: width	x: length																		
$y \leq 1/3L$	$X \leq a$																		

NO.	Item	Criterion	AQL
08	Cracked glass	No crack is allowed.	1.5
09	Backlight elements	9.1 Illumination source flickers when lit. 9.2 Spots or scratches that appear when lit must be judged. Using LCD spot, lines and contamination standards. 9.3 Backlight doesn't light or color is wrong.	1.5 1.5 0.65
10	Bezel	No scratches with W>0.1 and Length>2.5mm.	1.5
11	PCB、COB	11.1 COB seal may not have pinholes larger than 0.2mm or contamination. 11.2 COB seal surface may not have pinholes through to the IC. 11.3 The height of the COB should not exceed the height indicated in the assembly diagram. 11.4 There may not be more than 2mm of sealant outside the seal area on PCB. And there should be no more than three places. 11.5 Parts on PCB must be the same as on the production characteristic chart, There should be no wrong parts, missing parts or excess parts. 11.6 The jumper on the PCB should conform to the product characteristic chart.	1.5 1.5 1.5 1.5 0.65 0.65
12	FPC	FPC damage per IPC guidelines.(IPC-A-610) Nicks or damage along the edges of the flexible printed circuitry and cutouts, providing the penetration does not exceed 50% of the distance from the edge to the nearest conductor to 2.5mm[0.1in], Whichever is less.	1.5
13	Soldering	13.1 No cold solder joints, missing solder connections, oxidation or icicle. 13.2 No short circuits in components on PCB or FPC. 13.3 Soldering per IPC guidelines.(IPC-A-610)	1.5 0.65

NO.	Item	Criterion	AQL												
14	Touch Panel Chipped glass	<p>Symbols: x: Chip length y: Chip width z: Chip thickness k: Seal width t: Touch Panel Total thickness a: LCD side length L: Electrode pad length</p> <p>14.1 General glass chip: 14.1.1 Chip on panel surface and crack between panels:</p>  <table border="1" data-bbox="451 725 1272 943"> <tr> <td>z: Chip thickness</td> <td>y: Chip width</td> <td>x: Chip length</td> </tr> <tr> <td>$Z \leq t$</td> <td>$\leq 1/2 k$ and not over viewing area</td> <td>$x \leq 1/8a$</td> </tr> </table> <p>⊙ Unit: mm ⊙ If there are 2 or more chips, x is the total length of each chip</p> <p>14.1.2 Corner crack:</p>  <table border="1" data-bbox="451 1323 1272 1541"> <tr> <td>z: Chip thickness</td> <td>y: Chip width</td> <td>x: Chip length</td> </tr> <tr> <td>$z \leq t$</td> <td>$\leq 1/2 k$ and not over viewing area</td> <td>$x \leq 1/8a$</td> </tr> </table> <p>⊙ Unit: mm ⊙ If there are 2 or more chips, x is the total length of each chip</p>	z: Chip thickness	y: Chip width	x: Chip length	$Z \leq t$	$\leq 1/2 k$ and not over viewing area	$x \leq 1/8a$	z: Chip thickness	y: Chip width	x: Chip length	$z \leq t$	$\leq 1/2 k$ and not over viewing area	$x \leq 1/8a$	1.5
z: Chip thickness	y: Chip width	x: Chip length													
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$z \leq t$	$\leq 1/2 k$ and not over viewing area	$x \leq 1/8a$													

NO.	Item	Criterion	AQL										
15	Touch Panel(Fish eye、dent and bubble on film)	<table border="1" data-bbox="456 315 987 524"> <thead> <tr> <th>SIZE(mm)</th> <th>Acceptable Q'ty</th> </tr> </thead> <tbody> <tr> <td>$\Phi \leq 0.2$</td> <td>Accept no dense</td> </tr> <tr> <td>$0.2 < D \leq 0.4$</td> <td>5</td> </tr> <tr> <td>$0.4 < D \leq 0.5$</td> <td>2</td> </tr> <tr> <td>$0.5 < D$</td> <td>0</td> </tr> </tbody> </table>  	SIZE(mm)	Acceptable Q'ty	$\Phi \leq 0.2$	Accept no dense	$0.2 < D \leq 0.4$	5	$0.4 < D \leq 0.5$	2	$0.5 < D$	0	1.5
SIZE(mm)	Acceptable Q'ty												
$\Phi \leq 0.2$	Accept no dense												
$0.2 < D \leq 0.4$	5												
$0.4 < D \leq 0.5$	2												
$0.5 < D$	0												
16	Touch Panel Newton ring	Newton ring dimension $\leq 1/2$ touch panel area and not affect font and line distortion($\leq 2.5\%$) , it is acceptable.	1.5										
17	Touch Panel Linearity	Less than 1.5% is acceptable.	1.5										
18	LCD Ripple	Touch the touch panel , can not see the LCD ripple. Pen: R 1.0mm silicon rubber. Operation Force: 80g	1.5										
19	General appearance	19.1 Pin type must match type in specification sheet. 19.2 LCD pin loose or missing pins. 19.3 Product packaging must the same as specified on packaging specification sheet. 19.4 Product dimension and structure must conform to product specification sheet. 19.5 Product packaging shall be by trays sized to protect TFT and FPC cable. 19.6 Cable shall not be bent during transportation. 19.7 Top tray must be empty.	0.65 0.65 0.65 0.65										

11. Handling Precaution

11.1 Handling of LCM

Avoid external shock.

Don't apply excessive force on the surface.

Liquid in LCD is hazardous substance, do not lick or swallow. When the liquid is attaching to your hand, skin, cloth, etc., wash it thoroughly and immediately.

Don't operate it above the absolute maximum rating.

Don't disassemble the LCM.

The operators should wear protections whenever he/she comes into contact with the module.

Never touch any of the conductive parts such as the LSI pads, the copper leads on the PCB and the interface terminals with any parts of the human body.

The modules should be kept in antistatic bags or other containers resistant to static for storage.

The module is coated with a film to protect the display surface, be careful when peeling off this protective film since static electricity may be generated.

11.2 Storage

Store it in an ambient temperature of $25\pm 10^{\circ}\text{C}$, and in a relative humidity of $50\pm 10\%\text{RH}$. Don't expose to sunlight or fluorescent light.

Store it in a clean environment, free from dust, active gas, and solvent.

Store it in anti-static electricity container.

Store it without any physical load.

11.3 Soldering

Use only soldering irons with proper grounding and no leakage.

Iron: no higher than $280\pm 10^{\circ}\text{C}$ and less than 3 sec during hand soldering.

Rewiring: no more than 2 times.

12. Packing Method

No.	Item	Dimensions(mm)	Quantity	Remark
1	LCM Module	229.6*149.3*2.9	12PCS	
2	PALLET	345*285*175 (include 12pcs products/one pallet)	1PCS	
3	CARTON	385*315*227 (include 12pcs products/one carton)	1PCS	