

5			
4	Shading Tape	1	
3	FPC	1	
2	IC(ST7687s)	1	
1	LCD	1	
ND	Part Name	Quantity	

CUSTOMER APPROVAL:



DRAWN:

LPH

2012-07-24

CHECKED:

CSG

2012-07-24

APPROVED:

HHS

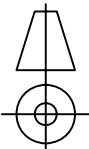
2012-07-24

LCM NO: AM0186R-11

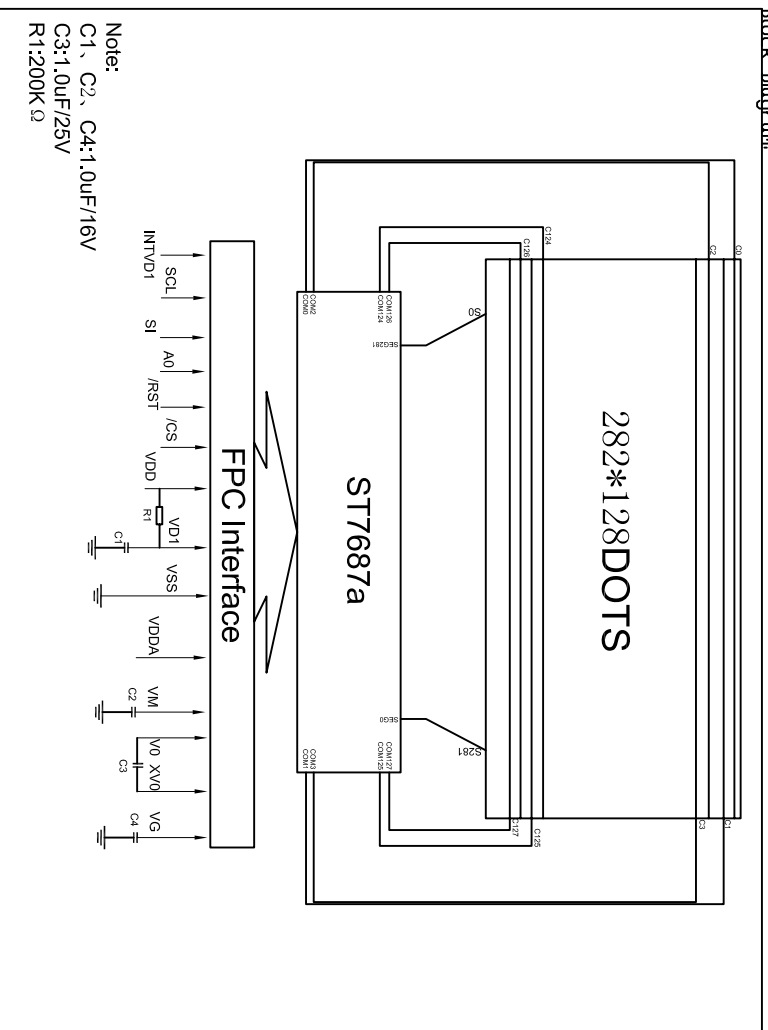
Rev: 00 UNITS: mm

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Block Diagram



Note:
 C1、C2、C4:1.0uF/16V
 C3:1.0uF/25V
 R1:200K Ω

DISPLAY TYPE: FSTN,Reflective,Positive
 VIEWING DIRECTION: 6:00
 DRIVER IC: **ST7687a**(4-wire SPI)
 LOGIC VOLTAGE(VDD): **1.8~3.0V,INTVD1=VSS**
3.0~3.3V,INTVD1=VDD

ANALOG VOLTAGE(VDDA):3.3V
 LCD DRIVE VOLTAGE(VLCD): 16V
 DRIVING METHOD: 1/128 DUTY, 1/12 BIAS
 OPERATING TEMPRATURE: -10° ~ +60°C
 STORAGE TEMPRATURE: -20° ~ +70°C
 INTERFACE CONNECTOR: FPC
 ALL UNMARKED TOLERANCE: $\pm 0.3\text{mm}$



Pin Description(4-wire SPI)

PIN	Symbol	Function Description
1	NC	No connection.
2	ESD1	ESD pad, connect to ground.
3	INTVD1	When VDD=1.8~3.0V,INTVD1=VSS. When VDD=3.0~3.3V,INTVD1=VDD.
4	SCL	Serial clock.
5	SI	Serial data input.
6	A0	Data/Command select.
7	/RST	Reset.(Active low)
8	/CS	Chip select.(Active low)
9	VDD	Digital power input
10	VDI	Voltage regulator for digital circuit.
11	VSS	Power ground.
12	VDDA	Analog power input.
13	VM	ID pin of LCD bias voltages.
14	V0	LCD driver supply voltages.
15	XV0	
16	VG	Bias driver supply voltages.
17	ESD2	ESD pad, connect to ground.
18	NC	No connection.

F00Modify the driver IC from D00.

- D00Modify the length and logic of the FPC from A05
 - A05Modify the ITD layout.
 - A04Modify the FPC layout and update the drawing.
 - A03Modify the FPC layout and update the drawing.
 - A02Modify the driver IC and update the drawing.
 - A01Add component on the FPC & modify the FPC logic.
 - A00: Original Edition
- Revision History:

CUSTOMER APPROVAL:



DRAWN:

YXH

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